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Substitute for form 1449A/PTO
INFORMATION DISCLOSURE Complete if Known 10/071373 **Application Number** STATEMENT BY APPLICANT February 8, 2002 Filing Date ąs necessary) First Named Inventor Howard, Jason 2816 2124 HOV 2 1 2003 13 **Group Art Unit** Linknown Chat NRP 2 8 2003 **Examiner Name** Sheel HADEMAS Attorney Docket No: 884.584US1 Technology Center 2100

US PATENT DOCUMENTS .						
Examiner Initial	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
ab	US-5,117,133	05/26/1992	Luebs, Richard	307	471 -	12/18/1990
CD	US-5,612,632	03/18/1997	Mahant-Shetti, S., et al.	3 26	46-	11/29/1994
CD	US-5,867,049	02/02/1999	Mohd, Bassam	327	-200-	11/21/1996
OD.	US-5,978,827	11/02/1999	Ichikawa, T	7 08	709	04/10/1996
CP	US-6,060,910	05/09/2000	Inui, S.	326-	98	08/07/1998
CO	US-6,121,807	09/19/2000	Klass, E. F., et al.	327	218-	05/24/1999
ĊD	US-6,181,180	01/30/2001	Chen, Zhanping , et al.	327	211	06/28/1999
OP	US-6,397,240	05/28/2002	Fernando, J S., et al.	Z08	603	02/18/1999
CO	US-6,437,602	08/20/2002	Friend, David, et al.	326	-93	07/12/2001
CP.	US-6,578,063	06/10/2003	Kojima, N , et al.	708	708	06/01/2000
CD	US-6,584,485	06/24/2003	Aoki, N, et al.	708	708	04/14/2000

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T²

OTHER DOCUMENTS NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T	

EXAMINER

DATE CONSIDERED

01/08/05

Examiner	Date Considered
	01/08/05
*Substitute Disclosure Statement Form (PTO-1449)	

^{**}EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Serial No. Unknown 10/07/37 3

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

Applicant: Jason M. Howard et al.

Atty. Docket No.: 884.584US1

Filing Date: Herewith 02/08/02

Group: Unknown 2124

OTHER DOCUMENTS

**Examiner
- Initial

Form 1449*

(Including Author, Title, Date, Pertinent Pages, Etc.)

Lee, K.T., et al., "1 GHz Leading Zero Anticipator Using Independent Sign-Bit Determination Logic", 2000 Symposium on VLSI Circuits Digest of Technical 194-195, (2000)"Accelerating Pipelined Integar and Floating-Point Luo, Z., et al., Accumulations in Configurable Hardware with Delayed Addition Techniques", IEEE Transactions on Computers, 49 (3), 208-218, (March 2000) "Multiply-Add Fused Risc Architectures for DSP Panneerselvam, G., et al., Applications", IEEE Pac Rim, pp. 108-111, (1993) "Flow-Through Latch and Edge-Triggered Flip-Flop Hybrid Partovi, H., et al., Elements", Proceedings of the IEEE International Solid-State Circuits Conference, Digest of Technical Papers and Slide Supplement, NexGen Inc., Milpitas, CA, 40 pgs., (1996)

Examiner Date Considered OI / 18/05